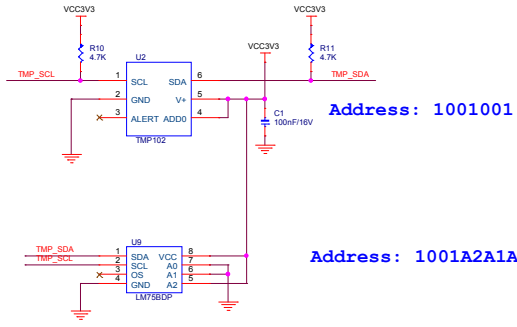
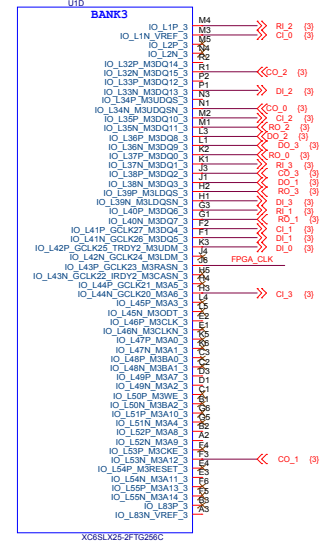
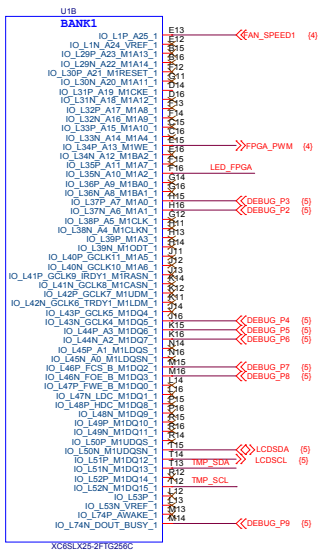
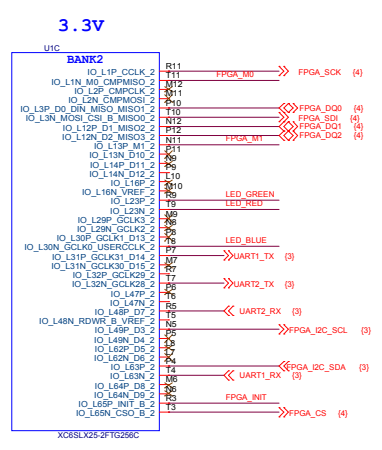
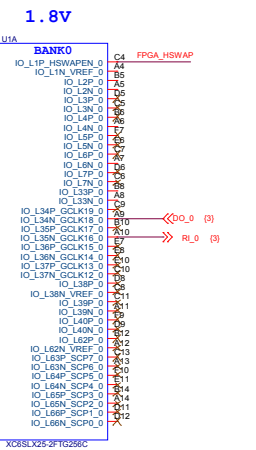


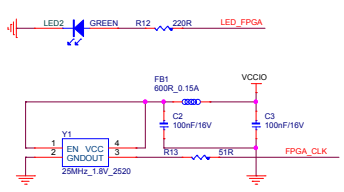
RO/CO/DO和RI/CI/DI是以HASH板芯片定义的  
RO/CO/DO指HASH输出,MM板输入  
RI/CI/DI指MM板输出,HASH输入

3.3V 1.8V



Address: 1001001

Address: 1001A2A1A0

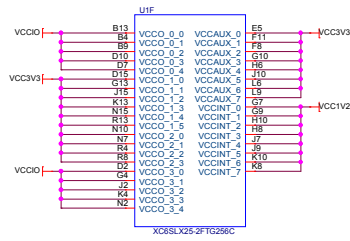
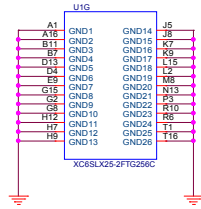
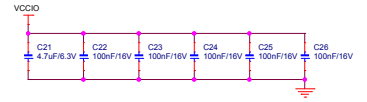
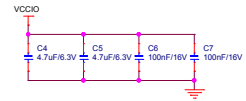
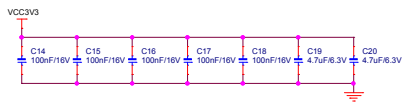
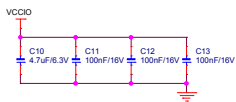
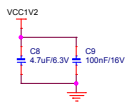


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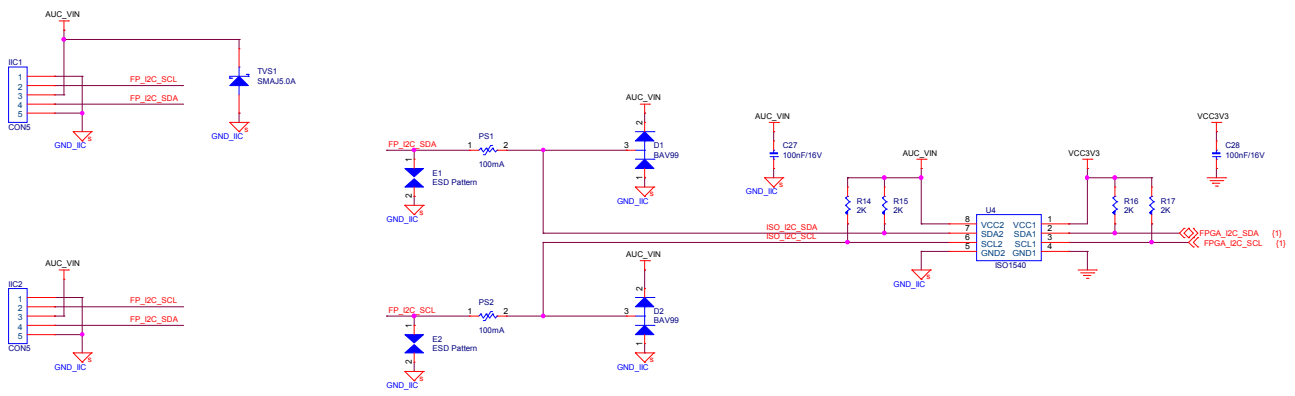
**Canaan**

MM851

Size: 01 - FPGA IO  
Date: Monday, March 11, 2019  
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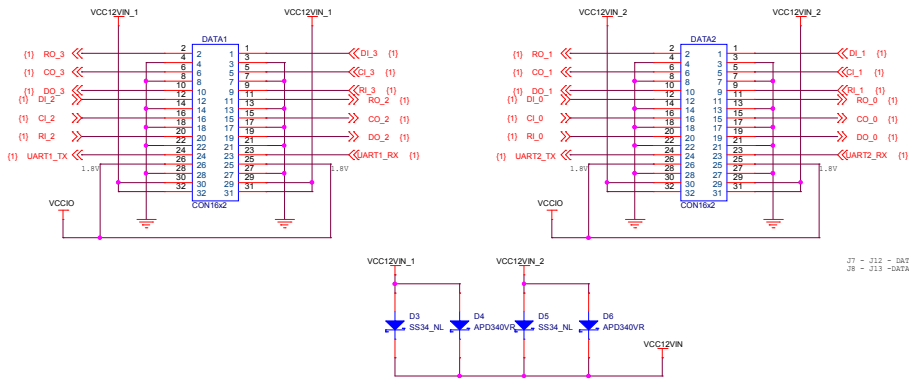
IIC



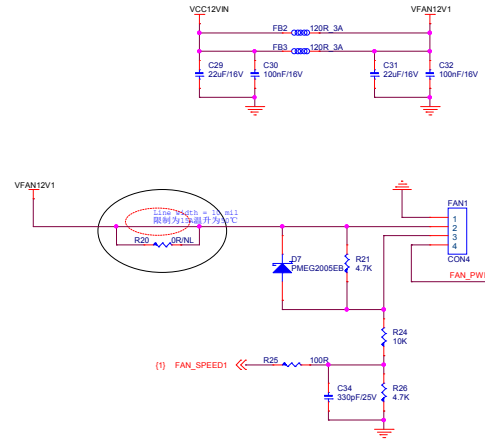
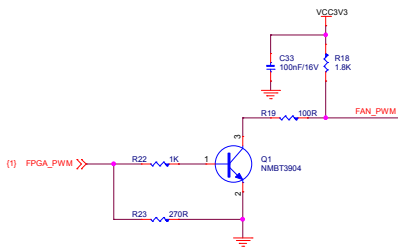
IO Header & UART & POWER

UART pin definition is from FPGA side. UART\_TX means TX on FPGA, UART\_RX means RX on FPGA

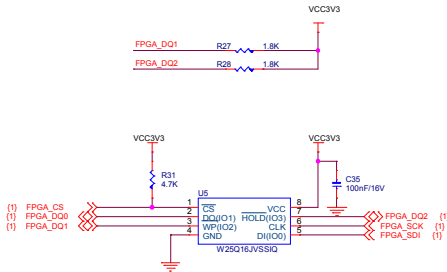
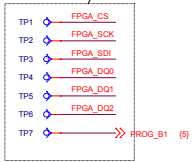
RO/CO/DO和RI/CI/DI是以HASH板芯片定义的  
RO/CO/DO指HASH输出,MMIO板输入  
RI/CI/DI指MMIO板输出,HASH输入



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MM851				
Size	Page Name	03 - I2C & IO & UART		Rev
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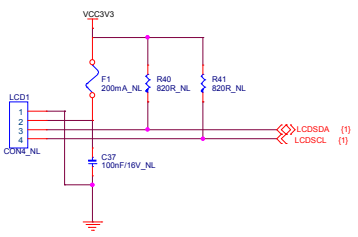
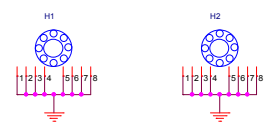
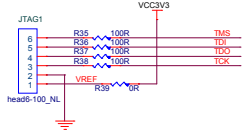
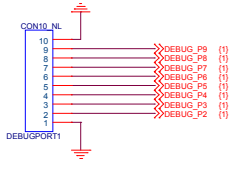
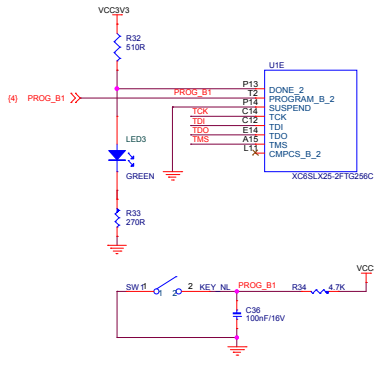
放在工裝容易做的地方



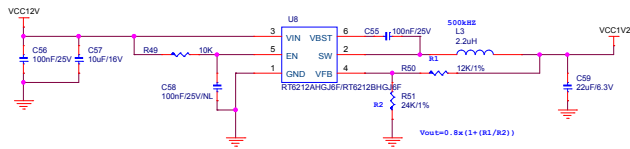
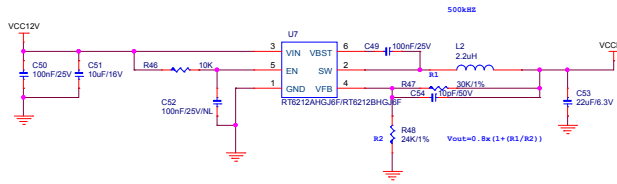
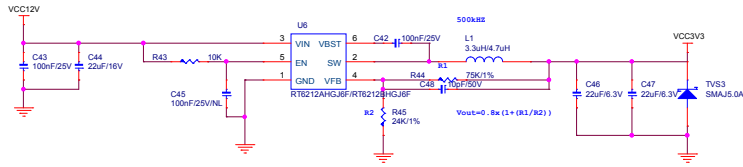
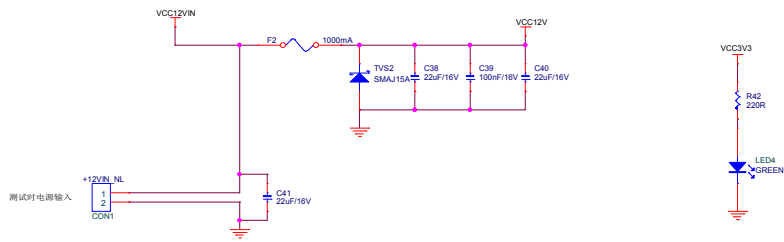
Layout Note:  
Termination Resistors. Close to FLASH.

靠近终端放置

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		北京市海淀区中关村软件园23#307		
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Custom	05 - JTAG DEBUG & MISC.			Rev1.0
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File		MM851	
Size	Page Name	Rev	Rev1.0
Customer	06 - POWER		
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